

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hongyong Zhang  
 Serial No. : 09/362,808  
 Filed : July 28, 1999  
 Title : METHOD OF FABRICATING SEMICONDUCTOR DEVICE

Art Unit : 2814  
 Examiner : Shrinivas H. Rao  
 Conf. No. : 7320

**MAIL STOP RCE**

Commissioner for Patents  
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INFORMATION DISCLOSURE STATEMENT

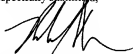
Please consider the references listed on the enclosed PTO-1449 form. Foreign patent documents and non-patent literature are enclosed; cited U.S. patents and patent application publications will be provided on request.

Submitted herewith is an English translation of the following foreign language references, or portions thereof:

Desig. ID	Source
AL	JAPAN 02-025024 – Full translation
AM	JAPAN 02-278749 – Full translation
AN	JAPAN 05-275373 – Full translation
AO	JAPAN 06-175156 – Full translation
AP	JAPAN 07-058107 – Full translation

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,



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Date: 6/27/06

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